Workshop on "Arduino and FPGA Based Embedded System Design" 23 to 27 June 2025

Program Schedule:

Day and Date	Time	Module/Activity	Instructor/TA
Day 1 Date: 23 rd June 2025	Lecture (9:30 AM to 11:30 AM)	Introduction to Arduino and Embedded Systems Introduction to Embedded Systems Overview of Arduino Platform Arduino IDE and Basic Programming	Prof. Rutu Parekh/ Prof Yash Agrawal and TAs
	Lecture cum Lab (11:30 AM–1:30 PM)	Setting up Arduino IDE Writing and uploading your first Arduino sketch (Blink LED)	
	Lecture (2:30 PM -4:30 PM)	 Arduino Hardware and Software Fundamentals Arduino Architecture and Microcontroller Basics Digital and Analog I/O Basic Electronic Components and Circuit Design Project Planning and Management 	
	Lecture cum Lab (4:30 PM– 6:30 PM)	Interfacing LEDs and Push Buttons Digital and Analog Input and Output Programming	
Day 2 Date: 24 th June 2025	Lecture (9:30 AM to 11:30 AM)	 Advanced Arduino Programming Serial Communication and Data Logging Working with Libraries and Timers Serial Communication with PC Data Logging to SD Card 	Prof. Rutu Parekh/ Prof Yash Agrawal and TAs
	Lecture cum Lab (11:30 AM–1:30 PM)	Building a Simple Android App to Control LED via Bluetooth Sending Sensor Data from Arduino to Android App	
	Lecture (2:30 PM -4:30 PM)	 Sensors and Actuators Interfacing Introduction to Sensors and Actuators Interfacing Temperature, Humidity, and Light Sensors Motor Control with Arduino 	
	Lecture cum Lab (4:30 PM– 6:30 PM)	Building a Basic Home Automation System Environmental Monitoring System with Multiple Sensors	

Day 3 Date: 25 th June 2025	Lecture (9:30 AM to 11:30 AM)	 Cloud Integration with Arduino Introduction to IoT and Cloud Platforms Sending Data from Arduino to Cloud (Blynk, ThingSpeak) 	Prof. Rutu Parekh/ Prof Yash Agrawal and TAs
	Lecture cum Lab (11:30 AM–1:30 PM)	Setting up Blynk or ThingSpeak Account Sending Sensor Data to Cloud and Visualizing	
	Lecture (2:30 PM -4:30 PM)	Introduction to Verilog HDL Basics and Structure Modeling Styles	
	Lecture cum Lab (4:30 PM– 6:30 PM)	 Setting up ISE Environment Implementation of Modeling Styles in Verilog 	
Day 4 Date: 26 th June 2025	Lecture (9:30 AM to 11:30 AM)	Introduction to Operators in Verilog	Prof Yash Agrawal/ Prof. Rutu Parekh/ and TAs
	Lecture (11:30 AM–1:30 PM)	 Combination and Sequential system designs Introduction to FPGA Embedded board 	
	Lecture cum Lab (2:30 PM -4:30 PM)		
	Lecture cum Lab (4:30 PM- 6:30 PM)		
Day 5 Date: 27 th June 2025	Lecture (9:30 AM to 11:30 AM)	 FSM Designs for Embedded Systems Advanced HDL modeling, processor fundamentals 	Prof Yash Agrawal/ Prof. Rutu Parekh/ and TAs
	Lecture (11:30 AM–1:30 PM)	 Discussion of High-end Applications - Image Processing, Audio Processing, Hardware Accelerators, Neural Network Designs Project Development 	
	Lecture cum Lab (2:30 PM -4:30 PM)	Lab on Embedded Project Development using Verilog HDL and FPGA	
	Lecture cum Lab (4:30 PM– 6:30 PM)		